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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/075,218	02/14/2002	Thomas S. Kobayashi	SC11931TP	8301
23125	7590	05/17/2007	EXAMINER	
FREESCALE SEMICONDUCTOR, INC.			FARAHANI, DANA	
LAW DEPARTMENT			ART UNIT	PAPER NUMBER
7700 WEST PARMER LANE MD:TX32/PL02			2891	
AUSTIN, TX 78729			MAIL DATE DELIVERY MODE	
			05/17/2007 PAPER	

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

<b>Office Action Summary</b>	Application No.	Applicant(s)
	10/075,218	KOBAYASHI ET AL.
Examiner	Art Unit	
Dana Farahani	2891	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

1) Responsive to communication(s) filed on 2/28/07.  
 2a) This action is **FINAL**.                            2b) This action is non-final.  
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

4) Claim(s) 1-7,9,11-18 and 20-22 is/are pending in the application.  
 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.  
 5) Claim(s) 1-7,9,18 and 20-22 is/are allowed.  
 6) Claim(s) 11-17 is/are rejected.  
 7) Claim(s) \_\_\_\_\_ is/are objected to.  
 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

9) The specification is objected to by the Examiner.  
 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.  
     Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
     Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
 a) All    b) Some \* c) None of:  
 1. Certified copies of the priority documents have been received.  
 2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

1) <input type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)	5) <input type="checkbox"/> Notice of Informal Patent Application
Paper No(s)/Mail Date _____	6) <input type="checkbox"/> Other: _____

**DETAILED ACTION**

***Claim Rejections - 35 USC § 103***

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 11, 12 and 17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lee et al. (US Patent 6,222,212), hereinafter Lee, in view of Nagarajan et al., hereinafter Nagarajan (US Patent 6,519,844).

Regarding claim 11, Lee discloses in figure 8A a semiconductor device comprising a substrate 803 having first and second circuitry 811 and 812, respectively, formed therein; and a fuse 813; the first circuit has a contact area, where it is connected to 805; the second circuit has a second contact area; the fuse is formed overlying a passivation layer 807, the fuse having a third contact area, above 808, which is electrically coupled to the first contact area of the first circuit, and the fuse having a fourth contact area, above 808, which is electrically coupled to the second contact area of the second circuit, wherein the first contact area of the first circuit and the second contact area of the second circuit are no longer electrically connected if the fuse is open-circuited; a first interconnect 805 for electrically connecting the first contact area to a first portion of the fuse; and a second interconnect for electrically connecting the second contact area to a second portion of the fuse, a passivation layer 807 overlying at least a portion of the substrate, wherein the fuse is formed overlying the passivation layer.

Lee does not disclose a packaging material selected from the group consisting of a mold compound and an under fill is over the fuse.

Nagarajan discloses in figure 1D, a mold compound 24 is over a chip 16. Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to form a mold over the chip 802 of the Lee's structure in order to add to the mechanical and chemical strength of the fuse package (see Nagarajan, column 3, lines 38-40).

Regarding claim 12, recessed areas, as shown in the figure, is formed in the passivation layer, and portion of the fuse (where connectors 808 are connected to) are in the recess areas.

3. Claims 13-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lee in view of Nagarajan, as applied to claim 11 above, and further in view of Weber et al., hereinafter Weber (US Patent 6,218,279).

Lee in view of Nagarajan render obvious the claimed invention, as discussed above, except for expressly disclosing the fuse comprises aluminum and a metal nitride.

Weber discloses a fuse wherein it comprises titanium nitride and aluminum (see column 2, lines 24-25). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to use the fuse of the Weber reference in order to benefit from the novel properties of the fuse such as adjustable resistance and easy manufacturing (see column 2, lines 7-12).

***Allowable Subject Matter***

4. Claims 1-7, 9, 18, and 20-22 are allowed.

The reason for indication of allowability of these claims is the inclusion therein of the limitations of first and second interconnect and the circuits and their relationship with the fuse; and a packaging material selected from the group consisting of a mold compound and an underfill is in contact with the fuse.

***Response to Arguments***

5. Applicant's arguments with respect to the claims have been considered but are moot in view of the new ground(s) of rejection.

***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dana Farahani whose telephone number is (571)272-1706. The examiner can normally be reached on M-F 9:00AM - 5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bill Baumeister can be reached on (571)272-1722. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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